

ABSTRACT OF THE DISCLOSURE

Disclosed is a duplexing method in a system duplexed into a first unit and a second unit. The first unit includes a first processor and a first local memory, and the second unit includes a second processor and a second local memory. The method according to the present invention comprises the steps of: providing a duplexing channel for concurrently accessing and selectively accessing the first local memory and the second local memory between the first unit and the second unit; providing a duplexing control logic section accessible to the first local memory and the second local memory through the duplexing channel; and concurrently accessing the first local memory and the second local memory through the duplexing channel by the duplexing control logic section based on a request for memory accessing by an active processor, which is either the first processor or the second processor.